Notice of Allowability	Application No.	Applicant(s)	
	10/828,328	SORIMACHI, HARUO	
	Examiner	Art Unit	
	Bradley K. Smith	2891	
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this or other appropriate communica GHTS. This application is subje	application. If not included tion will be mailed in due course.	
1. This communication is responsive to		•	
2. ☑ The allowed claim(s) is/are <u>1-4</u> .	•		
3. The drawings filed on 21 April 2004 are accepted by the Ex	kaminer.	·	
 4. Acknowledgment is made of a claim for foreign priority unanalysis. a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Application No	D	· the
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a re ENT of this application.	ply complying with the requiremen	ts -
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give)F
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") mus (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.	on's Patent Drawing Review(P	ne Office action of	
each sheet. Replacement sheet(s) should be labeled as such in the	he header according to 37 CFR 1.1	21(d).	
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	5. ☐ Notice of Inform 6. ☐ Interview Summ	al Patent Application (PTO-152) arv (PTO-413)	
3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail	Date	
Paper No./Mail Date <u>4/21/04</u> 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ☐ Examiner's State 9. ☑ Other <u>search no</u>	Brad Smith Primary Examiner Art Unit 2891	

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows: In the Title delete "Method of production of semiconductor device" and replace with - - The method of vias on an electrode terminal - -

2. The following is an examiner's statement of reasons for allowance: the prior art of record neither teaches nor suggests electrolessly plating the electrode terminals in order to cover the surfaces of the electrode terminals forming a protective film, grinding the backside of the wafer, covering the wafer with a resin, focusing a laser beam above the electrode terminal to form via holes in the laminate exposing the bottom protective film and filling the via holes by electroplating.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Yamamoto et al. (US Pregrant Publication 2002/0081530) disclose forming via holes above an electrode by focusing an laser beam on the laminate. Nishiyama (US Pregrant Publication 2002/0192867) disclose the formation of chip-like electronic components.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Bradley K. Smith whose telephone number is (571) 272-1884. The examiner can normally be reached on 10-6 Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Baumeister can be reached on (571) 272-1722. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Brad Smith

Primary Examiner

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